



DW8505A

Low Power LED Driver



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1. General Description

DW8505A is an instant On/Off LED driver for low power LED applications. At DW8505A output stage, one regulated current port is designed to provide a uniform and constant current sink for driving LEDs within a large range of V_F variations. DW8505A easily provides users with a consistent current source. Users may adjust the output current up to 100mA through an external resistor R_S , which gives users flexibility in controlling the light intensity of LEDs. In addition, users can precisely adjust LED brightness from 0% to 100% via output enable (EN) with Pulse Width Modulation. DW8505A can accommodate an input voltage up to 40V.

■ Features

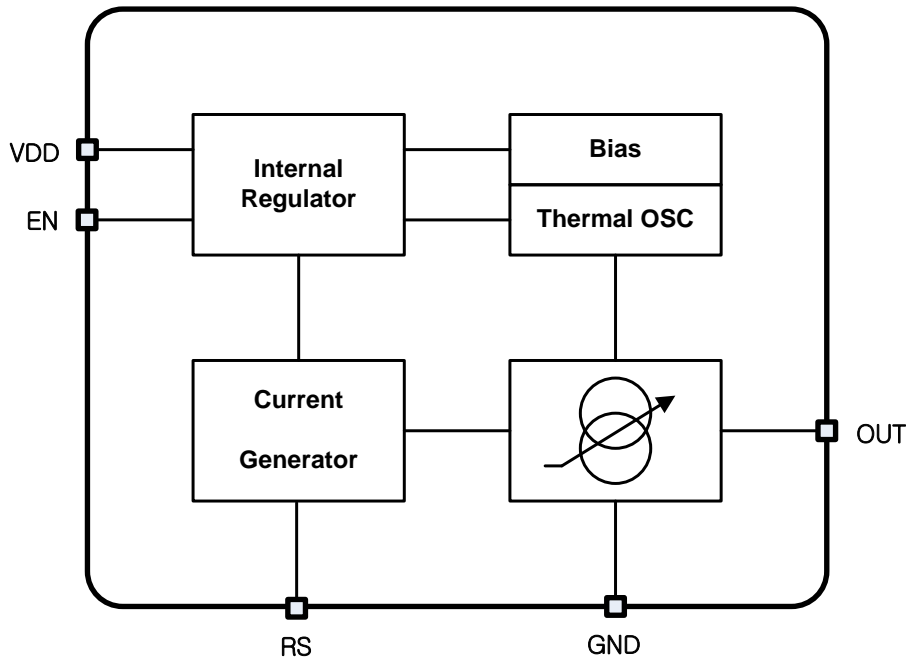
- providing a constant output current regardless of input voltage or load voltage changes
- 5V to 40V supply voltage
- adjustable output current up to 100mA
- Built-in thermal derating circuit
- Available PWM dimming control
- Output current adjusted through an external resistor
- SOT23-5L Package

■ Applications

- LED light bulbs
- Signage and decorative LED lighting
- General lighting of flat panel displays
- RGB backlighting LED driver
- Current stabilizer with DC/DC or AC/DC
- Automotive lighting
- General purpose constant current source

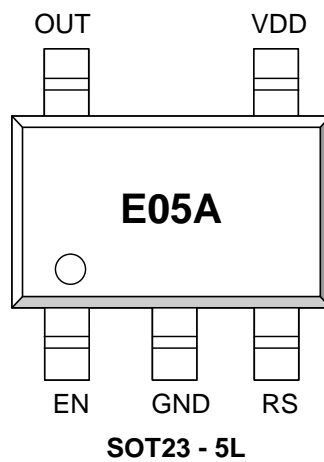


2. Block Diagram



3. Pin Information

■ Pin Placement



■ Pin Description

No.	Pin Name	Description	Note
1	EN	Output stage enable control pin. If not use dimming, connect to VDD. Dimming control is available by PWM(Pulse width modulation) under 30KHz typically.	
2	GND	Ground	
3	RS	Sets output current. Connect a resistor from RS to GND to set the LED bias current	
4	VDD	Supply voltage input.	
5	OUT	Output pin. Sink current is decided by the current on R_{SET} connected to RS.	



4. Absolute Maximum Ratings⁽¹⁾

Symbol	Parameter	Ratings
VDD	Supply voltage	41V
V _{EN}	Enable voltage	41V
V _{OUT}	Output voltage	23V
V _{RS}	Reference voltage	5V
θ_{JA} ⁽²⁾	Package thermal resistance	95.03 °C/W
T _{JOPR}	Junction Operating temperature	-40~+125 °C
T _J	Junction temperature	+150 °C
T _{STG}	Storage Temperature	-55~+150 °C

Note (1) Stresses above the max. Values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Maximum ratings are absolute ratings; exceeding only one of these values may cause irreversible damage to the integrated circuit.

(2) θ_{JA} is measured in the convection at Ta=30 °C on a high effective thermal conductivity test board(4 Layers, 2S2P) of JEDEC 51-7 thermal measurement standard. PCB dimension is 100mmx100mmx1.6mm and 4 layers.

5. Recommended Operating Condition

Symbol	Parameter	Min.	Typ.	Max.	Unit
VDD	Supply voltage	5		40	V
V _{EN}	Enable voltage			40	V
I _{OUT}	Output sink current		60		mA



6. Electrical Specification

(VDD = 24V, typical values are at TA=+25°C, unless otherwise specified.)

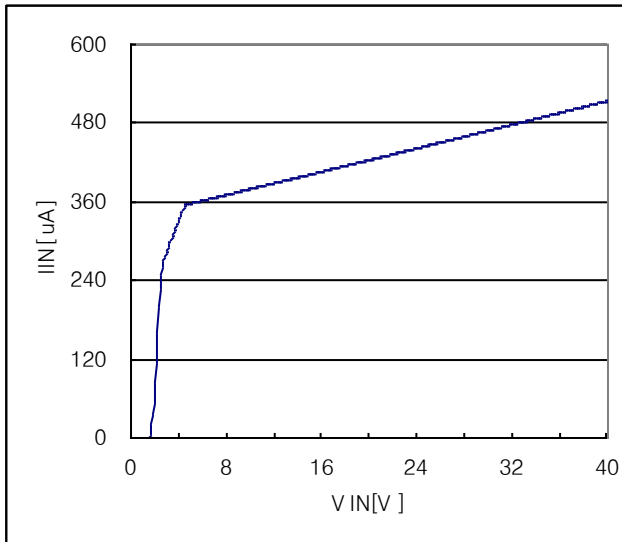
Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Input Supply Voltage	V _{DD}		5	-	40	V
Output Linearity Voltage	V _{OUT_LINE}	V _{DD} =24V, I _{SET} = 60mA,	-	-	3	V
Output Current	I _{OUT}		-	-	100	mA
Quiescent Current	I _{Q_ON}	EN = 24V	0.3	0.45	1	mA
	I _{Q_OFF}	EN = 0V	85	120	250	uA
EN Input Leakage Current	I _{EN_LIK}		30	45	60	uA
Input High Voltage	V _{IH}		2	-	-	V
Input Low Voltage	V _{IL}		-	-	0.8	V
LED Output Dropout Voltage	V _{DROP}	V _{DD} =24V , I _{SET} =60mA	-	100	-	mV
Thermal Derating	T _D		-	140	-	°C
Thermal Derating Hysteresis	T _{DHYS}		-	15	-	°C
R _{SET} Voltage	V _{SET}		0.58	0.61	0.64	V
Output Current	I _{OUT}	11.91KΩ	58.2	60	61.8	mA

Note : Output dropout voltage : 90% x I_{OUT}

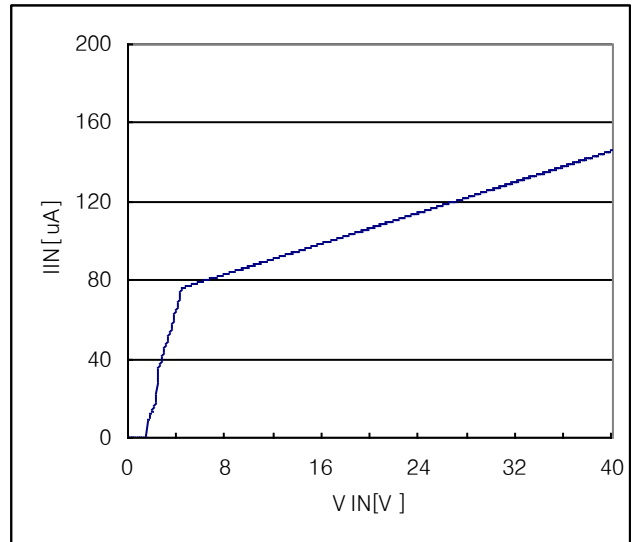


7. Typical Operating Characteristics

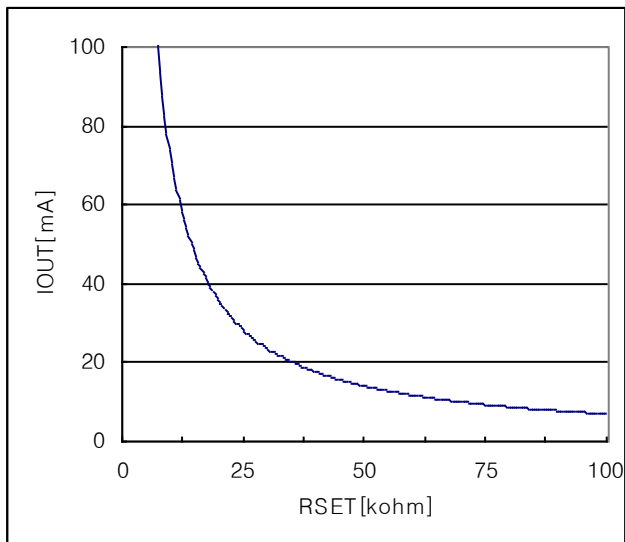
($V_{DD} = 12V$, $V_{OUT} = 2V$, typical values are at $T_A = +25^\circ C$, unless otherwise specified.)



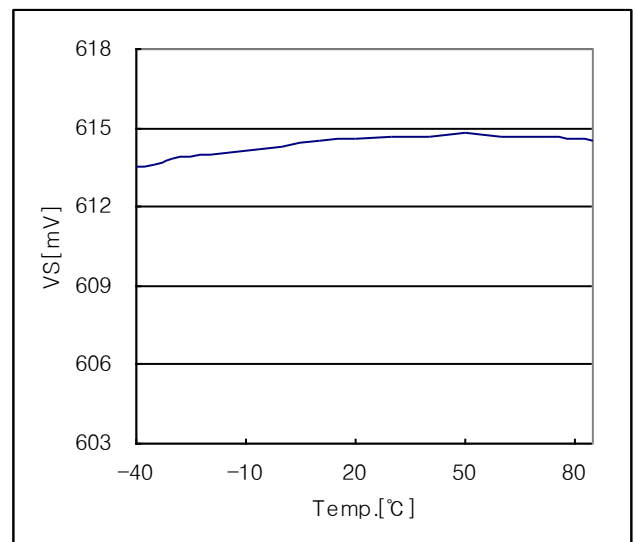
Quiescent vs. VIN



Ishutdown vs. VIN



IOUT vs. Rset

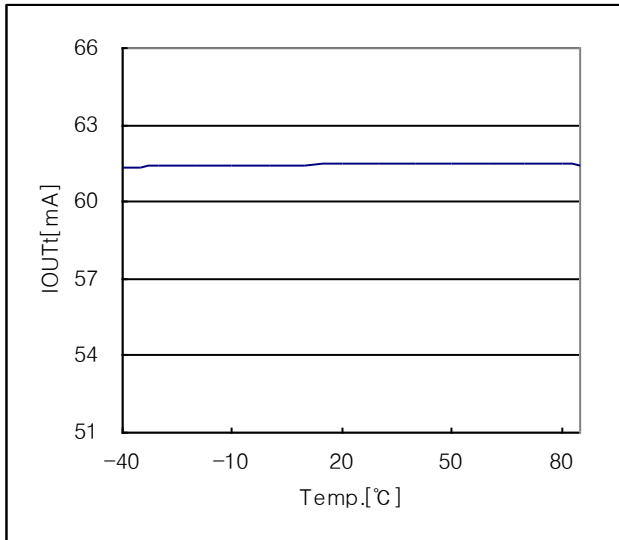


Vreference vs. Temperature

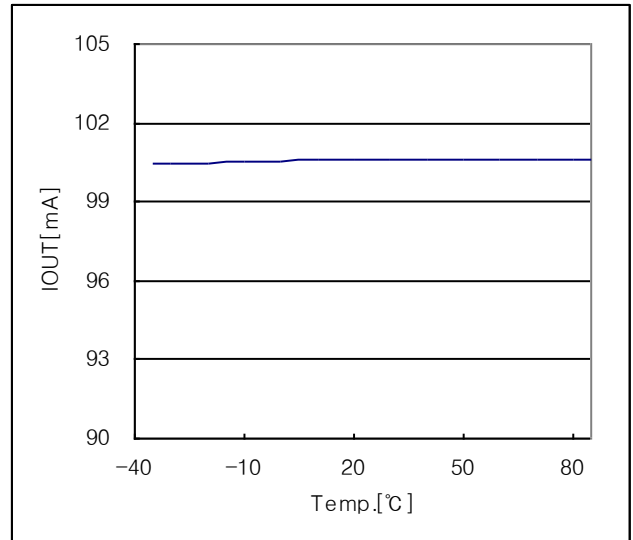


7.1. Typical Operating Characteristics

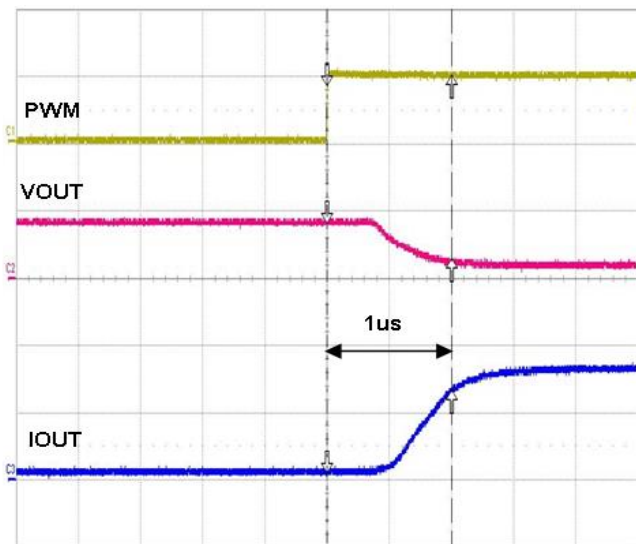
($V_{DD} = 12V$, $V_{OUT} = 2V$, typical values are at $T_A = +25^\circ C$, unless otherwise specified.)



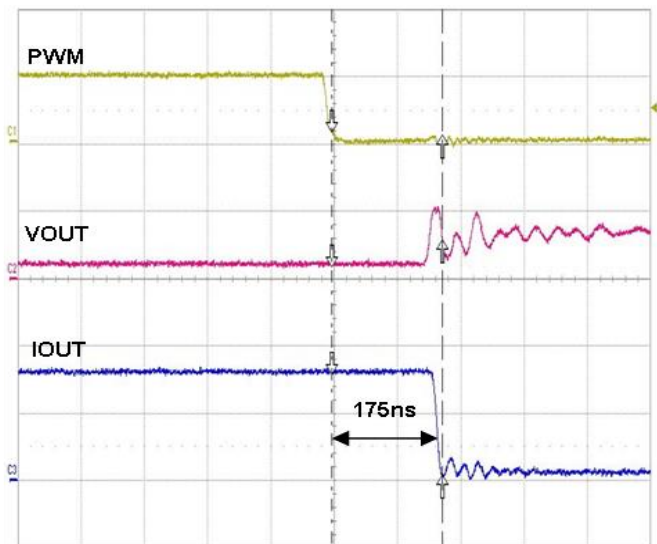
IOUT vs. Temperature



IOUT vs. Temperature



PWM Rising Time



PWM Falling Time



8. Typical Applications circuit

(LED $V_F = 3.3V$, $I_F = 20mA$)

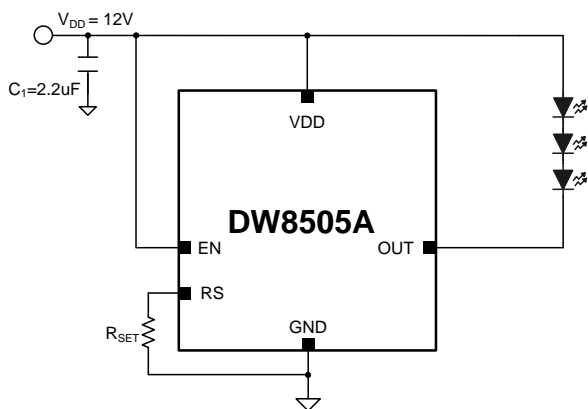


Figure 1. VDD=12V

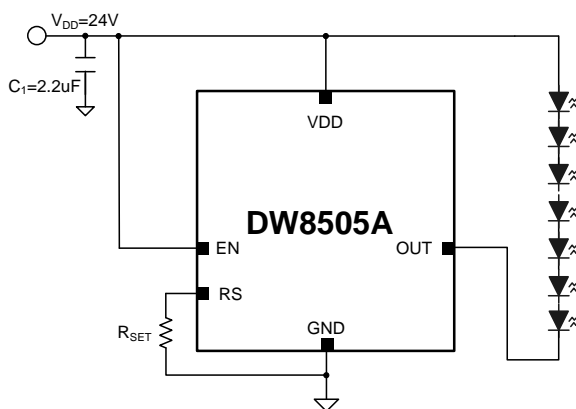


Figure 2. VDD=24V

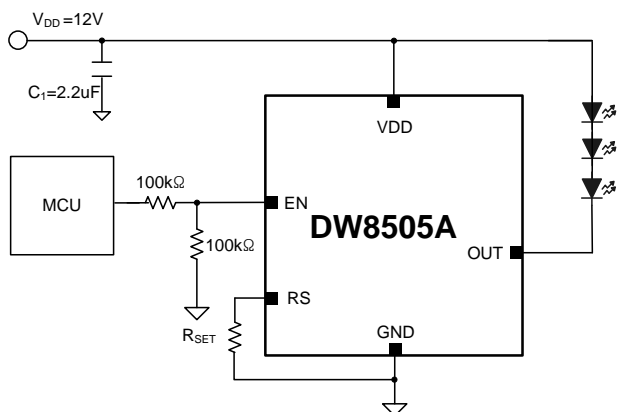


Figure 3. PWM Typical Application

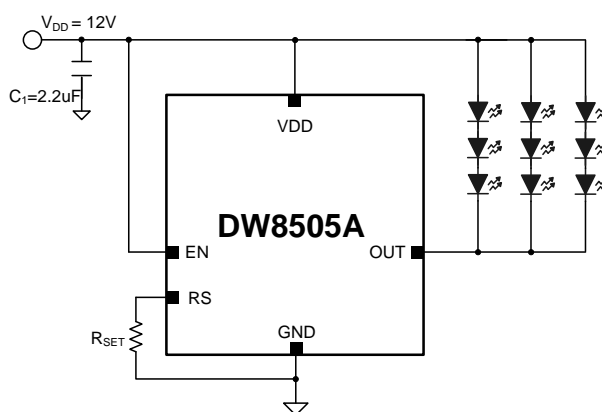


Figure 4. VDD=12V, 9 LED

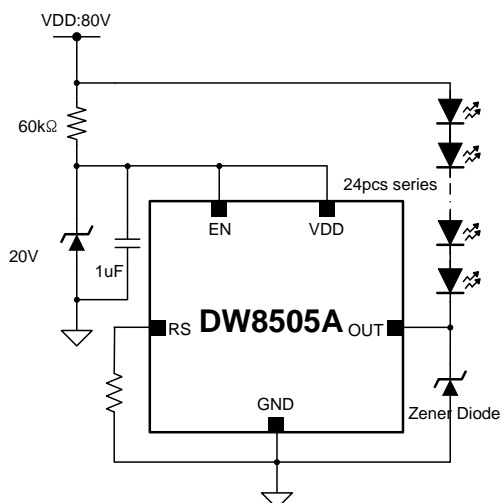


Figure 5. High Voltage Application (VDD=80V)

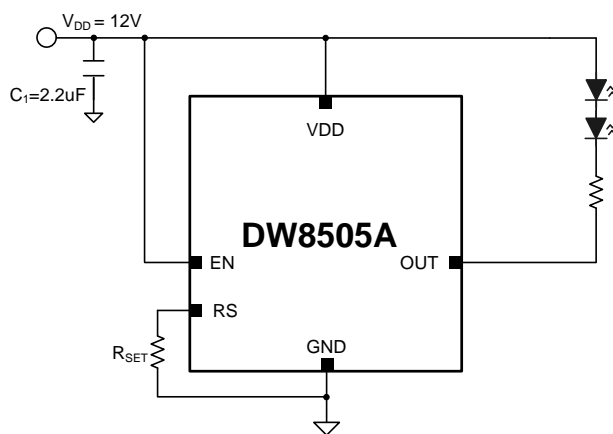


Figure 6. VDD=12V, 2 LED



9. Detailed Descriptions

■ Setting Output Current

Output Current is fixed by resistor, The value of resistor is as below.

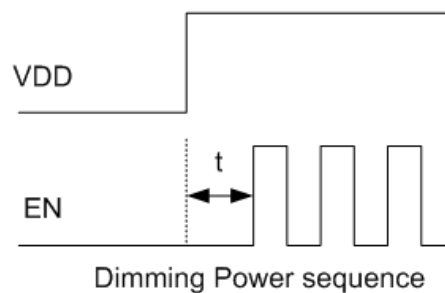
Current[mA]	Resistor[KOhm]	Current[mA]	Resistor[KOhm]	Current[mA]	Resistor[KOhm]
10	75	33	22	61	11.6
12	62	36	20	65	11
15	51	40	18	70	10.2
17	43	45	16	71	10
19	39	48	15	78	9.1
20	36	50	14.39	80	8.82
22	33	51	14	87	8.2
24	30	55	13	90	7.78
27	27	59	12	94	7.5
30	24	60	11.91	100	7

■ Power Sequence

There is an electrostatic diode between VDD and EN.

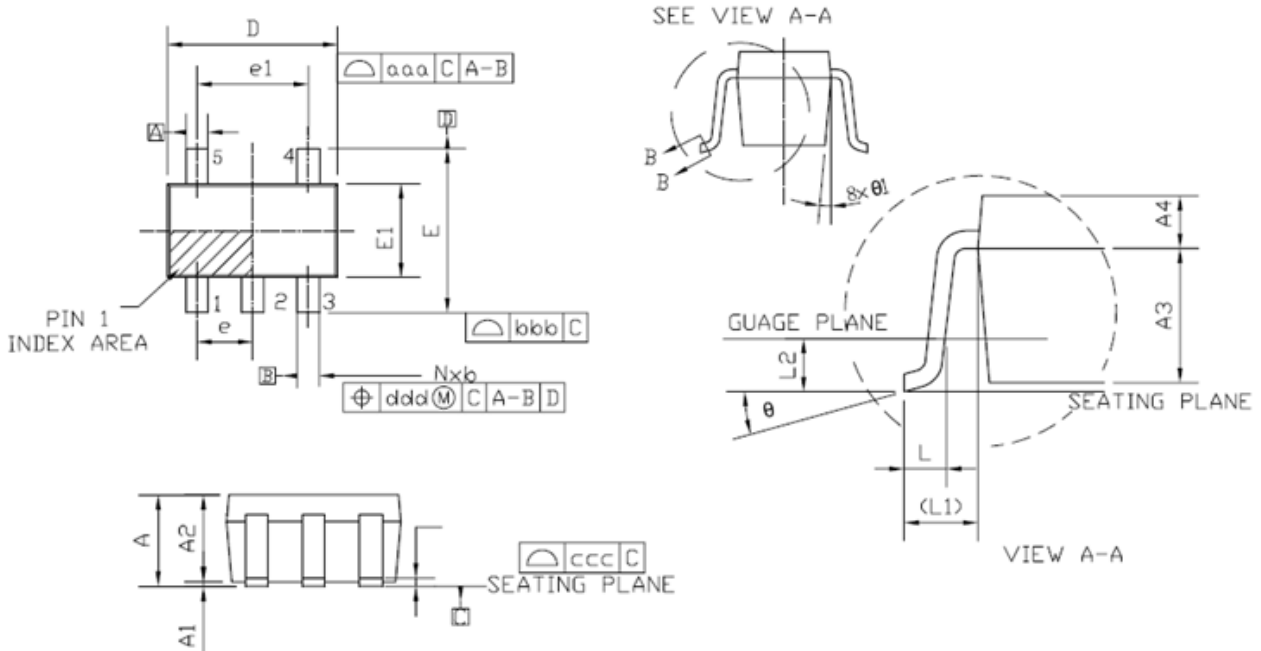
When dimming control, It must input EN signal after inputs VDD. ($t \geq 1\text{ms}$)

If not use Dimming control, EN connect to VDD.



10. Package Dimension

Package Name : SOT23-5L
 Package Size : 2.9mm * 2.8mm, Thickness : 1.3mm
 Pin Pitch : 0.95mm



COMMON DIMENSION			
SYMBOL	IN MILLIMETERS		
	MIN	NOMAL	MAX
A	0.90	1.30	1.40
A1	0	0.075	0.15
A2	0.90	1.20	1.25
A3	0.637	0.787	0.812
A4	0.263	0.413	0.438
b	0.30	0.35	0.50
b1	0.30	0.40	0.45
c	0.08	0.152	0.22
c1	0.08	0.13	0.20
D	2.90 BSC		
e	0.95 BSC		
e1	1.90 BSC		
E	2.80 BSC		
E1	1.60 BSC		
L	0.30	0.45	0.60
L1	0.60 REF.		
L2	0.25 BSC		
θ	0°	4°	8°
θ1	5°	10°	15°
aaa	0.15		
bbb	0.20		
ccc	0.10		
ddd	0.10		

